Clean Version

In the Specification:

Beginning on page 3, line 1:

5   Another application in portable devices for providing high density archival storage is described in co-pending United States Patent Application serial number 09/875,356, filed June 5, 2001, entitled "Write-Once Memory," the disclosure of which is hereby incorporated herein by reference. The memory system disclosed therein, referred to as portable inexpensive rugged memory (PIRM), aims to provide high capacity write-once memory at

10 low cost for archival storage. This is realized in part by avoiding silicon substrates, minimizing process complexity and lowering areal density. The memory system includes a memory module formed of a laminated stack of integrated circuit layers constructed on plastic substrates. Each layer contains cross-point diode memory array, and sensing of the data stored in the array is carried out from a separate integrated circuit remotely from the

15 memory module.

Beginning on page 5, line 12:

       In one embodiment, the electrical marking device comprises at least one layer that is a display layer that is partially visually altered to provide a display of information, such as a

20 display of the subject matter and name of the content of the data and the amount of memory storage that has been used.

Beginning on page 14, line 3:

       Fabrication of the PIRM memory module described herein is preferably done in

25 accordance with the methods of fabrication provided in co-pending United States Patent Application serial number 09/875,572, filed on June 5, 2001, entitled "Fabrication Techniques for Addressing Cross-Point Diode Memory Arrays," the disclosure of which is hereby incorporated herein by reference.

30 Beginning on page 16, line 1:

       The display memory is addressed, written, and read in the same fashion as the other memory layers. Preferred methods for addressing, reading and writing to the memory module are given in co-pending United States Patent Application serial number 09/875,496, filed June 5, 2001, entitled "Addressing and Sensing a Cross-Point Diode Memory Array,"

35 and co-pending United States Patent Application serial number 09/875,828, filed June 5,